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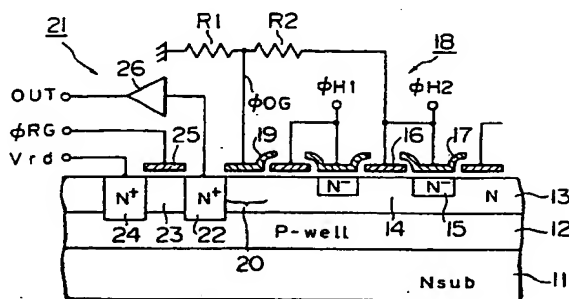
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(54) Charge transfer apparatus and driving method thereof

(57) A charge transfer apparatus which is capable of reliably improving the transfer efficiency of an output gate section while preventing the occurrence of coupling to output waveforms. A two-phase driving-type charge transfer apparatus is constructed as follows. Transfer clock $\phi H2$ used for driving the stage one prior to the final stage of a charge transfer section is divided at a predetermined ratio through the use of two resistors. This causes the generation of drive pulse ϕOG in phase with and of a smaller amplitude than transfer clock $\phi H2$. A gate electrode of an output gate section is driven by this drive pulse ϕOG .

FIG. 1





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EUROPEAN SEARCH REPORT

Application Number
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Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
A	EP 0 405 401 A (SONY CORP) 2 January 1991 * column 2, line 20 - line 6 * * column 4, line 31 - line 39 * * claims 1,4,5,8,10 * * figure 2 *	1,3,5,6,8,10	G11C27/04 G11C19/28
A	US 4 554 675 A (MIWADA KAZUO) 19 November 1985 * column 2, line 25 - column 3, line 31 * * figure 3 *	1,2,5,8,10	
			TECHNICAL FIELDS SEARCHED (Int.Cl.6)
			G11C
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 22 January 1999	Examiner Colling, P
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**ANNEX TO THE EUROPEAN SEARCH REPORT
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